IN THE CLAIMS

1. (currently amended) A device for processing data signals, comprising:

a plurality of input interfaces each ~~inputting~~ receiving an input signal; and

a multiplexing circuit multiplexing a plurality of output signals from the plurality of input interfaces,

wherein said each of the plurality of input interfaces comprises:

a storing part storing the input signal; and

an extracting part extracting ~~said~~ data signals included in said input signal from said storing part and outputting said data signals to said multiplexing circuit,

wherein said extracting part receives storage state information ~~of said input signal~~ indicating an amount of data stored in said storing part from said storing part and outputs said data signals to said multiplexing circuit based on the amount of data indicated by the storage state information.

2. (currently amended) The device as claimed in claim 1, wherein:

said storing part includes a first memory part, a second memory part, and a third memory part;

said input signal is consecutively stored ~~sequentially to~~ in an order of an arrangement of the first memory part, the second memory part, and the third memory part; and

said second memory part signals said extracting part of said storage state information of said second memory part.

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3. (previously presented) The device as claimed in claim 1, wherein when an empty release is notified from said storing part, said extracting part outputs said data signals, in which an invalid data signal is inserted to said input signal.

4. (previously presented) The device as claimed in claim 1, wherein when an empty release is notified from said storing part, said extracting part outputs said data signals in which an invalid data signal included in said input signal is deleted.

5. **(currently amended)** The device as claimed in claim 1, wherein said extracting part comprises:

a monitoring part monitoring said data signals ~~input~~ from said storing part;

a data determining part determining said data signals based on a notice of said storage state information from said storing part and a notice of validity of said data signals from said monitoring part; and

an invalid data generating part generating invalid data to insert into said input signal,

wherein said invalid data generating part inserts said invalid data into said input signal in response to a determination notice from said data determining part.

6. **(currently amended)** The device as claimed in claim 1, wherein said extracting part comprises:

a monitoring part monitoring said data signals ~~output~~ from said storing part;

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a no-data code determining part determining a no-data code based on a notice of said

storage state information from said storing part and a notice of validity of said data signals from

said monitoring part; and

a deleting part deleting said no-data code included in said input signal in response to a

determination notice.

7. – 8. (canceled)

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